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DATE MAILED: 12/28/2005

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/774,905	02/09/2004	Eamon Nash	7061 8772		
55740 75	90 12/28/2005		EXAMINER		
GAUTHIER & CONNORS, LLP 225 FRANKLIN STREET BOSTON, MA 02110		•	NGUYEN, MATTHEW VAN		
			ART UNIT	PAPER NUMBER	
,			2838		

Please find below and/or attached an Office communication concerning this application or proceeding.

AK
411/

	Application	No.	Applicant(s)	
·	10/774,905		NASH, EAMON	
Office Action Summary	Examiner	-	Art Unit	
·	MATTHEW	V. NGUYEN	2838	
The MAILING DATE of this communication a	ppears on the o	over sheet with the	correspondence addres	s
Period for Reply A SHORTENED STATUTORY PERIOD FOR REP WHICHEVER IS LONGER, FROM THE MAILING - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory perions after the reply within the set or extended period for reply will, by state than three months after the maine earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS 1.136(a). In no event od will apply and will e tute, cause the applica	S COMMUNICATION, however, may a reply be expire SIX (6) MONTHS froation to become ABANDON	N. imely filed m the mailing date of this community (35 U.S.C. § 133).	
Status				
1) Responsive to communication(s) filed on 29	February 2004	! <u>.</u>		
,— · · —	nis action is nor			
3)☐ Since this application is in condition for allow	vance except fo	or formal matters, p	rosecution as to the me	rits is
closed in accordance with the practice under	r Ex parte Qua	yle, 1935 C.D. 11, 4	453 O.G. 213.	
Disposition of Claims				
4) Claim(s) 1-19 is/are pending in the application 4a) Of the above claim(s) is/are withdred 5) Claim(s) is/are allowed. 6) Claim(s) 1-19 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and	rawn from cons			
Application Papers				
9)☐ The specification is objected to by the Exami	ner.			. -
10) The drawing(s) filed on is/are: a) a		objected to by the	Examiner.	
Applicant may not request that any objection to the	ne drawing(s) be	held in abeyance. S	ee 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the	•		•	
Priority under 35 U.S.C. § 119				
12) Acknowledgment is made of a claim for foreignal All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume * See the attached detailed Office action for a list	ents have been ents have been riority documen eau (PCT Rule	received. received in Applica ts have been receive 17.2(a)).	ition No ved in this National Staç	je
Attach mout(c)				
Attachment(s) 1) Notice of References Cited (PTO-892)	4) Interview Summa	ry (PTO-413)	
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0 Paper No(s)/Mail Date 7/19/04. 	08) 5	Paper No(s)/Mail		···
U.S. Patent and Trademark Office PTOL-326 (Rev. 7-05) Office	Action Summary	F	Part of Paper No./Mail Date 20	0051222

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- 1. The disclosure should be carefully reviewed and ensure that any and all grammatical, idiomatic, and spelling or other minor errors are corrected.
- 2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5, 8-12 and 14-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Jensen et al. (U.S. pat. No. 5,724,003).

With regard to claims 1-5, 8-12 and 14-19, Jensen et al. (i.e., Fig. 6) shows an RMS to DC converter system and a method thereof comprising a variable gain amplifier (412) receiving an input signal (410) and providing an amplifier output signal (416), a detector (418) receiving the amplifier output signal and providing a detector output signal (420) including an AC component, an error amplifier (424) receiving the detector output signal and providing an error amplifier output signal (510-up) having an AC component, a feedback circuit (510-back) coupled the error amplifier output signal and to the variable gain amplifier for providing a feedback signal to the variable gain amplifier including an AC component, the error amplifier (424) further including a reference input node (terminal +) for receiving a reference signal (REFERENCE) including DC component and AC component having a low frequency sine wave signal, and an amplifier feedback

circuit (C1) between the error amplifier output and an error amplifier input (terminal -) that receives the detector output signal.

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 6 and 7 rejected under 35 U.S.C. 103(a) as being unpatentable over Jensen et al. in view of Midya et al. (U.S. Pat. No. 6,348,781).

With regard to claims 6 and 7, Jensen et al. shows an RMS to DC converter system comprising all the claimed subject matter as discussed in the subparagraph 2 above, except for an RC output circuit connected to the output signal of the error amplifier.

Midya et al. discloses a converter apparatus (Fig. 5) in which an RC circuit (35K, 508) connected to the output signal (SIG) of the error amplifier (506).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to utilize the RC circuit coupled to the output signal of the error amplifier as shown by Midya et al. into the RMS to DC converter system of Jensen et al. for the purpose of obtaining a more stable signal at the output of the error amplifier.

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Claim 13 is rejected under 35 U.S.C. 103(a) as being unpatentable over Jensen 4

et al.

With regard to claim 13, Jensen et al. shows an RMS to DC converter system

comprising all the claimed subject matter as discussed in the subparagraph 2

above, except for the value of the capacitor (C1) in the amplifier feedback circuit

being less than 500 pF.

It would have been obvious to one having ordinary skill in the art at the time the

invention was made to choose the value of the capacitor less than 500 pF, since

it has been held that discovering an optimum value of a result effective variable

involves only routine skill in the art. In re Boesch, 617 F.2d 272, 205 USPQ 215

(CCPA 1980).

5. The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure.

Tam (U.S. Pat. No. 5,126,686), Matsumoto et al. (U.S. Pat. No. 5,319,804) and

Toyota et al. (U.S. Pat. No. 6,750,719) also disclose converter systems each of which

comprises substantial elements as recited in the claims of the instant application.

Any inquiry concerning this communication or earlier communications from the 6.

examiner should be directed to Matthew V. Nguyen whose telephone number is (571)

272-2081.

Any inquiry of a general nature or relating to the status of this application or

proceeding should be directed to the Group receptionist whose telephone number is

(571) 272-2800.

MARY EXAMINER

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